

AMENDMENTS TO THE CLAIMS:

1. (Currently amended) A terminal device comprising:
a control section for controlling the terminal device;
a first real-time clock which is built in the control section; and
a second real-time clock which is provided outside the control section, wherein:
the control section ~~obtains~~ is responsive to the terminal device being in a first operation mode to obtain real-time information from the first real-time clock, ~~when the terminal device is in a first operation mode, and~~
the control section ~~obtains~~ is further responsive to the terminal device being in a second operation mode to obtain real-time information from the second real-time clock, ~~when the terminal device is in a second operation mode.~~
2. (Currently amended) A terminal device as claimed in claim 1, ~~wherein further comprising a signal line directly connecting the second real-time clock is directly connected to the control section by, a signal line.~~
3. (Currently amended) A terminal device as claimed in claim 1, ~~wherein further comprising a functional device connecting the second real-time clock is connected to the control section, via a functional device.~~
4. (Currently amended) A terminal device as claimed in claim 1, ~~wherein further comprising a functional device having the second real-time clock is built therein, in a~~

~~functional device which is connected to the control section.~~

5. (Currently amended) A terminal device as claimed in claim 1, wherein the control section is further responsive to the terminal device changing from the second operation mode to the first operation mode to transfer the real-time information obtained from the second real-time clock ~~is transferred~~ to the first real-time clock and thereafter ~~the control section obtains to obtain~~ the real-time information from the first real-time clock.

6. (Currently amended) A terminal device as claimed in claim 5, wherein the control section is further responsive to the real-time information obtained from the second real time clock being transferred to the first real-time clock to restore the real-time information of the first real-time clock ~~is restored~~ by use of the real-time information transferred from the second real-time clock.

7. (Currently amended) A terminal device as claimed in claim 1, wherein further comprising a first power source for powering the control section and a second power source for powering the second real-time clock, ~~are powered by different power sources.~~

8. (Currently amended) A terminal device as claimed in claim 1, wherein the real-time information ~~at least includes~~ comprises time information and date information.

9. (Currently amended) A terminal device as claimed in claim 1, wherein:

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the terminal device is responsive to the control section operating normally to enable
the first operation mode; ~~is enabled when the control section is operating normally, and~~
the terminal device is responsive to the control section operating other than normally
to enable the second operation mode; ~~is enabled when the control section recovered from~~
~~failure.~~

10. (Currently amended) A terminal device as claimed in claim 1, wherein the control section is ~~implemented by~~ comprises a microcomputer which is built in the terminal device.

11. (Currently amended) A method for controlling a real-time clock of a terminal device, comprising: ~~the steps of:~~

~~a mode judgment step for judging whether the terminal device is in a first operation mode or a second operation mode;~~

~~a first information obtaining step in which a~~ when the terminal device is in the first operation mode, activating a control section of the terminal device obtains to obtain real-time
information from a first real-time clock which is built in the control section; ~~if the terminal device is in the first operation mode, and~~

~~a second information obtaining step in which~~ when the terminal device is in the second operation mode, activating the control section obtains to obtain real-time
information from a second real-time clock which is provided outside the control section; ~~if the terminal device is in the second operation mode.~~

12. (Original) A method as claimed in claim 11, wherein the second real-time clock is directly connected to the control section by a signal line.

13. (Original) A method as claimed in claim 11, wherein the second real-time clock is connected to the control section via a functional device.

14. (Original) A method as claimed in claim 11, wherein the second real-time clock is built in a functional device which is connected to the control section.

15. (Currently amended) A method as claimed in claim 11, ~~wherein further~~ comprising, when the terminal device changes from the second operation mode to the first operation mode, transferring the real-time information obtained from the second real-time clock is transferred to the first real-time clock, and thereafter the control section obtains the obtaining real-time information from the first real-time clock.

16. (Currently amended) A method as claimed in claim 15, ~~wherein further~~ comprising restoring the real-time information of the first real-time clock is restored by use of the real-time information transferred from the second real-time clock.

17. (Currently amended) A method as claimed in claim 11, ~~wherein further~~ comprising powering the control section by a first power source, and powering the second

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real-time clock are ~~powered by different~~ by a second power-sources source.

18. (Currently amended) A method as claimed in claim 11, wherein the real-time information ~~at least includes~~ comprises time information and date information.

19. (Currently amended) A method as claimed in claim 11, ~~wherein~~ further comprising:

~~the first operation mode is enabled~~ when the control section is operating normally, enabling the first operation mode; and

~~the second operation mode is enabled~~ when the control section ~~recovered from failure~~ is operating other than normally, enabling the second operation mode.

20. (Currently amended) A method as claimed in claim 11, wherein the control section ~~is implemented by~~ comprises a microcomputer which is built in the terminal device.